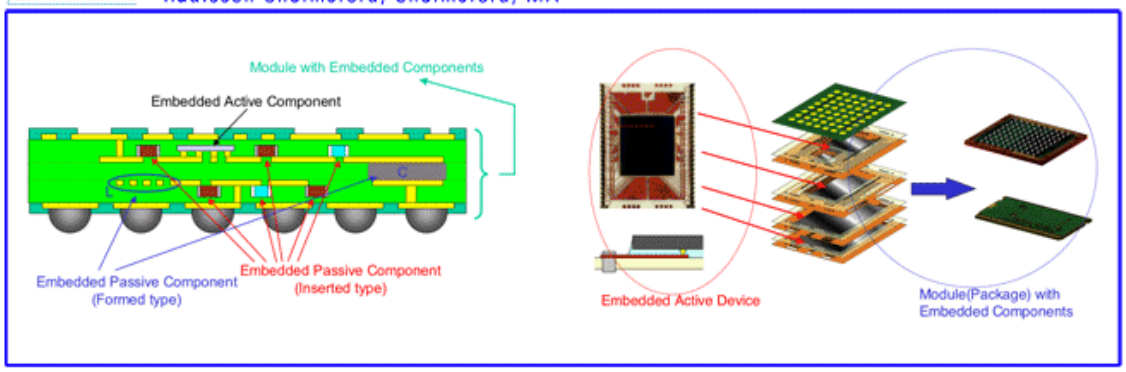
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Oak Mitsui Technologies will be presenting two papers on Embedded Passive Technology

-Substrate with Combined Embedded Capacitance and Resistance for Better Electrical Performance and Higher Integration

John Andresakis, Oak-Mitsui Technologies

-Reduction of Discrete Capacitors using Embedded Capacitance Layers: Comparison of Simulated Versus Actual number of Discrete Components Removed

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